ZJSC Series

3-Terminal Filters for Signal Line and DC Power Line Lead

FEATURES

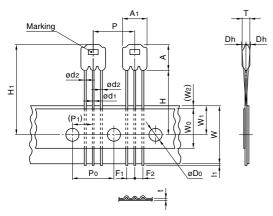
- With a lead pitch of 2.5mm (identical to the terminal pitch of DIP type ICs), the ZJSC series T-type EMC filters enable the design of high-density circuit board configurations using automated insertion.
- Their sharp cutoff characteristics provide effective highfrequency attenuation with minimum effect on passband signals, even when such signals are at close proximity to the trapband.
- The series are available in an extensive range of cutoff frequencies, enabling their use in a wide range of applications.
- The use of highly miniaturized inductor elements helps minimize variations in the mounted heights of circuit board components.
 This plus the use of round leads ensures superior clinchability and insertability, making this product ideal for use in automated mounting production lines.

PRODUCT IDENTIFICATION

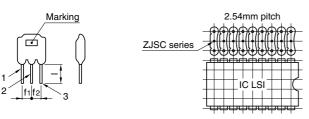
$$\frac{\text{ZJSC}}{\text{(1)}} - \frac{2\text{R2}}{\text{(2)}} - \frac{101}{\text{(3)}} - \frac{\text{TA}}{\text{(4)}}$$

- (1)Series name
- (2)Inductance value 2R2:2.2µH×2
- (3)Capacitance 101:100pF
- (4)Packaging style TA: Taping*
- Only used for bulk packaged products. This should become "TAH" when using TDK's AVI-SERT automatic assembly equipment feeder.

SHAPES AND DIMENSIONS TAPING SPECIFICATIONS



BULK SPECIFICATIONS TYPICAL MOUNTING EXAMPLE

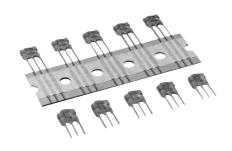


APPLICATIONS

Home electronic equipment, (TVs, VCRs, CD players, DAT players, electric musical instruments, PCs, etc.), office automation equipment (computers, terminals, stand-alone word processors, fax machines, etc.), factory automation equipmen

BASIC CHARACTERISTICS

Rated voltage Edc	50V max.	
Withstand voltage Edc	125V	
[Between terminal No.1, 3 to 2]		
Insulation resistance	10000M Ω min.	
[DC. 50V for 1min]		
Operating temperature range	−25 to +85°C	



Dimensions in mm

A 1	7.5max.	
Α	8max.	
T	2.4max.	
ød1	0.5±0.05	
ød2	0.6±0.05	
Р	12.7±1	
P ₀	12.7±0.3	
P1	6.35±0.4	
F1, F2	2.5+0.4, -0.1	
Δh	0±2	
W	18+1, -0.5	
W ₀	12±0.3	
W ₁	9±0.5	
(W ₂)	(4max.)	
Н	16±1	
H ₁	24.5max.	
øD0	4±0.2	
t	0.5±0.2	
l1	0.5max.	
I	5±1.5	
f1, f2	2.5±0.5	
	A T Ød1 Ød2 P P0 P1 F1, F2 Δh W W0 W1 (W2) H H1 ØD0 t I1	

- *1 The permissible cumulative pitch error is within ±1mm of 20 pitches.
- *2 The dimension at the end of the lead pitch is also within the tolerance.
- *3, *4 When TAH has been set in the taping specification *3 and *4, it becomes 20±1mm and 28mm max. respectively.
- *5 The tolerance of lead pitch is the dimensions when a lead is released from the tape. Not available for bulk packaging.



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ELECTRICAL CHARACTERISTICS

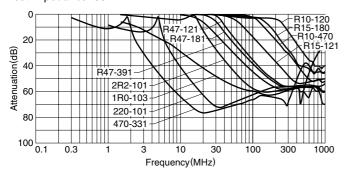
	Cutoff frequency	Frequency range(MHz)		Datad valtage	Rated current	Characteristic
Part No.	[Theoretical value] (MHz)ref.	25dB min.	40dB min.	— Rated voltage Edc(V)max.	(mA)max.	impedance Z_0 (Ω)ref.
ZJSC-R10-120	210	600 to 1000	900 to 1000	50	450	75
ZJSC-R10-470	100	300 to 1000	500 to 1000	50	500	65
ZJSC-R15-180	150	400 to 1000	700 to 1000	50	450	75
ZJSC-R15-121	50	150 to 1000	300 to 1000	50	500	50
ZJSC-R47-121	30	100 to 1000	200 to 500	50	500	90
ZJSC-R47-181	25	100 to 1000	150 to 500	50	500	70
ZJSC-R47-391	17	70 to 1000	150 to 500	50	500	50
ZJSC-1R0-103	2.3	15 to 1000	40 to 500	50	400	15
ZJSC-2R2-101	15	40 to 1000	70 to 400	50	300	210
ZJSC-220-101	5	10 to 500	15 to 300	50	100	660
ZJSC-470-331	1.8	4 to 500	7 to 300	50	60	530

⁻ The insertion loss response is measured across a 50Ω test impedance.

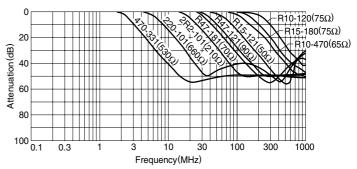
TYPICAL ELECTRICAL CHARACTERISTICS ATTENUATION vs. FREQUENCY CHARACTERISTICS

Glass epoxy coated double side mounting PCB(t=1.6mm)

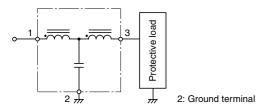
Test impedance: 50Ω



Characteristic impedance: 50 to $660\Omega\,$



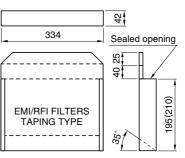
CIRCUIT DIAGRAM



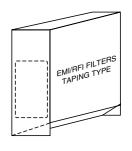
PACKAGING STYLE AND QUANTITIES

Packaging style	Quantity
Taping	2000 pieces/reel

PACKAGING STYLE (Ammo-pack)



INDICATES INTERIOR CONTENTS OF BOX



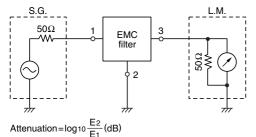
Dimensions in mm

 When using a TDK AVI-SERT automatic assembly equipment feeder, use dimension
 () to designate the "TAH" taping specification.

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TECHNICAL NOTES INSERTION ATTENUATION MEASUREMENT METHOD



E2: Set EMC filter in the circuit
E1: Leave EMC filter in the circuit

MOUNTING SUBSTRATE FOR MEASUREMENT

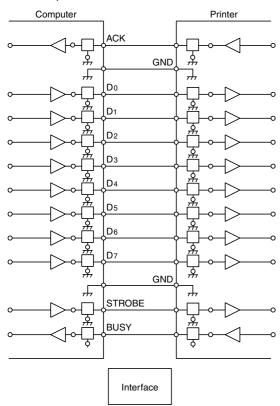
Mount on the glass fabric-backed epoxy resin double-sided through-hole substrate(t=1.6mm)

MEASUREMENT TEMPERATURE

+5 to +35°C

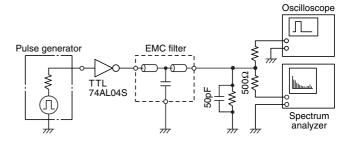
TYPICAL APPLICATIONS

An example of radiated noise suppressing circuit by connecting a PC and a printer.



EXAMPLES OF MEASURING NOISE SUPPRESSION EFFECT (Waveform spectrum)

(1)MEASUREMENT CIRCUIT

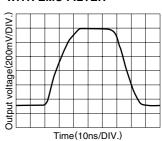


PULSE WAVEFORM

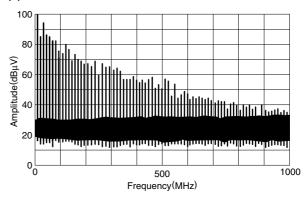
WITHOUT EMC FILTER

Output voltage (200m/VDIV.)

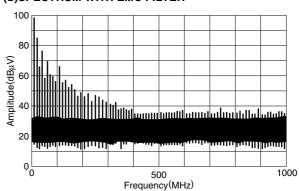
WITH EMC FILTER



(2)MEASUREMENT RESULTS (a)SPECTRUM WITHOUT EMC FILTER



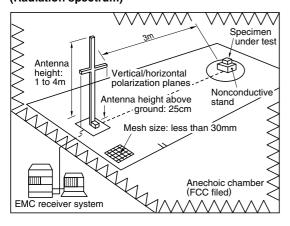
(b)SPECTRUM WITH EMC FILTER



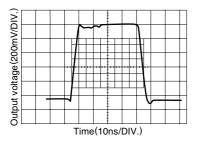
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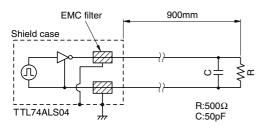
AN EXAMPLE OF MEASURING NOISE SUPPRESSION EFFECT (Radiation spectrum)



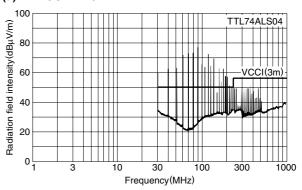
TTL OUTPUT WAVEFORM WITHIN A SHIELDING CASE



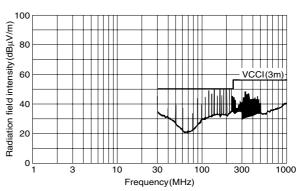
MODEL



RADIATION LEVEL (a)WITHOUT EMC FILTER



(b)WITH EMC FILTER



BICONICAL ANTENNA INDUCED WAVEFORM

